IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

APPLICANTS: James D. Kelly *et al.*

APPLICATION NO.: 10/006,939

FILING DATE: November 30, 2001

TITLE: BUS TRANSACTION REORDERING IN A COMPUTER SYSTEM HAVING UNORDERED SLAVES

EXAMINER: Gopal C. Ray

GROUP ART UNIT: 2181

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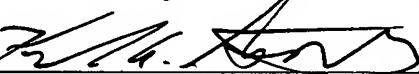
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RESPONSE A

Sir:

IN THE CLAIMS

All pending claims are set forth below. No amendments have been made.

1. (Unchanged) In a computer system having a system bus and having arbitration circuitry, multiple master devices including a system microprocessor, and multiple slave devices, all coupled to the system bus, a method of reordering system bus transactions, comprising the steps of:

receiving and queuing within a particular slave device a plurality of transactions;

within said arbitration circuitry, arbitrating between pending transactions based on arbitration policies including an arbitration policy that responses are received